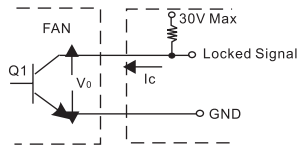




RD-2A

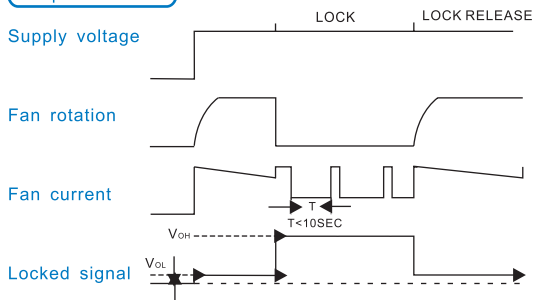
Output of locked signal

*Output type.....Open collector type
 *Electrical specification:



*Transistor Q1 at "ON" position
 Collector current..... $I_c=10\text{mA Max}$
 Saturation Voltage..... $V_{OL}=1.0\text{V Max}$
 (Between Collector and Emitter at $I_c=10\text{mA}$)
 *Transistor Q1 at "OFF" position
 Release Voltage..... $V_{OH}=30\text{V Max}$

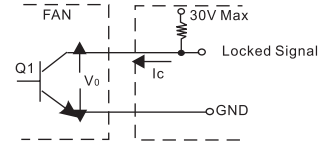
***Output waveform**



RD-2C

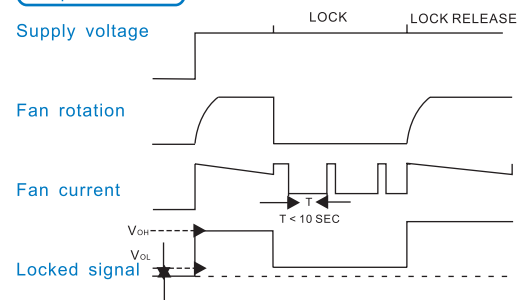
Output of locked signal

*Output type.....Open collector type
 *Electrical specification:



*Transistor Q1 at "ON" position
 Collector current..... $I_c=10\text{mA Max}$
 Saturation Voltage..... $V_{OL}=1.0\text{V Max}$
 (Between Collector and Emitter at $I_c=10\text{mA}$)
 *Transistor Q1 at "OFF" position
 Release Voltage..... $V_{OH}=30\text{V Max}$

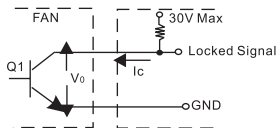
***Output waveform**



RD-2B

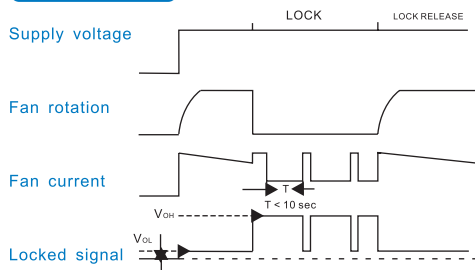
Output of locked signal

*Output type.....Open collector type
 *Electrical specification:



*Transistor Q1 at "ON" position
 Collector current..... $I_c=10\text{mA Max}$
 Saturation Voltage..... $V_{OL}=1.0\text{V Max}$
 (Between Collector and Emitter at $I_c=10\text{mA}$)
 *Transistor Q1 at "OFF" position
 Release Voltage..... $V_{OH}=30\text{V Max}$

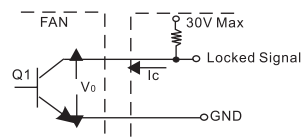
***Output waveform**



RD-2D

Output of locked signal

*Output type.....Open collector type
 *Electrical specification:



*Transistor Q1 at "ON" position
 Collector current..... $I_c=10\text{mA Max}$
 Saturation Voltage..... $V_{OL}=1.0\text{V Max}$
 (Between Collector and Emitter at $I_c=10\text{mA}$)
 *Transistor Q1 at "OFF" position
 Release Voltage..... $V_{OH}=30\text{V Max}$

***Output waveform**

